

PATENT
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IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application. An identifier indicating the status of each claim is provided. The listing is provided for reference.

Listing of Claims

1-7. (canceled)

8. (original) A data multiplexing device comprising:

a plurality of buffer memories which store a plurality of packet data strings constituting a plurality of data elements;

a multiplexing means which has a switch means for switching said buffer memories and which time-division multiplexes said plurality of packet data strings to provide an output by sequentially time-division switching said buffer memories with said switch means, and

a switch control means which selects, according to an input rate for said packet data strings, said plurality of buffer memories switchable by said switch means.

9. (original) A data multiplexing device according to claim 8, wherein said switch control means switchably controls said switch means to exclude a buffer memory for buffering lower priority information among said plurality of buffer memories, when said input rate is higher than a reference rate.

10. (original) A data multiplexing device according to claim 9, wherein said switch control means determines packet data containing EMM data to be lower priority

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information and excludes a buffer memory for buffering said packet data containing said EMM data among said switchable buffer memories.

11. (original) A data multiplexing device according to claim 10, wherein said switch control means excludes, in addition to said buffer memory for buffering said packet data containing said EMM data, a buffer memory for buffering packet data containing EPG data among said switchable buffer memories, when said input rate is still higher than said reference rate after said buffer memory for buffering said packet data containing said EMM data has been excluded.

12-122. (canceled)